

Extensive Portfolio of Flash Memory Patents For Sale

Source: Nif/T, LLC

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IP experts Nif/T, LLC today announced for license or outright sale an important portfolio containing 19 Flash Memory Patents

LOS GATOS, Calif. -- Flash memory today is more than just thumb drives and storage for MP3 players. Today it's everywhere - embedded in a myriad of consumer electronics, automotive systems, and home appliances, to name only a few applications. In all, the available portfolio comprises 17 US patents, 1 Taiwan patent, and 1 Singapore patent. These assets are segmented into 3 lots where each lot can be purchased or licensed either separately or together. Patented structures and methods focus on CACT (Channel Accelerated Carrier Tunneling) and TG (Tunnel Gun) technologies, as well as high temperature, high reliability embedded NVM structures. CACT and TG are enablers for advanced low voltage program and erase applications.

Lot 1:

US P/N **7,376,014** covers a nonvolatile memory whose reliability and endurance have been improved by reducing the impact of disturbs and also ensuring isolation from neighboring cells.

US P/N **7,583,530** covers structures for storing more than one bit in a cell – at differing locations using a non-spreading storage medium.

US P/N **7,149,125** and **7,227,786** cover location specific storage cell structures and operation, including low voltage programming and erase methods.

US P/N **7,224,620** and **7,193,900** cover programming and erase capability based on the CACT and TG Program erase methods for programming and erasing of a non-volatile memory cell.

US P/N **5,506,431** and **5,519,653** cover low voltage programming where the acceleration of carriers is used to reduce the voltage needed to overcome the potential barrier.

Lot 2:

US P/N **5,790,455**; **5,986,931**; **6,201,732**; **6,574,140**; **110307** (Taiwan); and **64015** (Singapore), are applicable to embedded and medium size NVM arrays with very high retention and read-write cycling capability.

US P/N **6,451,652** minimizes the masks used for self-alignment during manufacturing.

US P/N **6,411,545** covers a non-volatile latch for high reliability embedded applications.

Lot 3:

US P/N **6,534,816**; **6,479,863**; and **6,384,451** cover various tunneling charge injector functionalities for floating gate memory cells.

Further details about the Flash Memory portfolio for sale are available at:

<http://nif-t.net/services/patcat-flash.html>

To see other patents offered for sale by Nif/T clients, please visit:

<http://nif-t.net/services/patcat.html>

About Nif/T, LLC:

Since 1997, Nif/T has assisted over a hundred early-stage and emerging technology firms to achieve the goals of effective commercialization of new technologies and improved financial valuation based on a strong IP portfolio. Nif/T advises on [Strategic alliances](#); [Fund raising](#); [Intellectual property protection](#), and IP [assessment & monetization](#).

Nif/T leverages its significant experience in evaluating and dealing with IP to offer advice on how to monetize IP, either by licensing, selling, or asserting our clients' IP assets. Monetizing these valuable assets can provide additional financing, competitive position enhancement, or in some cases re-capture of investor equity value. Nif/T's approach includes a proprietary method for IP portfolio evaluation and uncovering potential transaction targets. In these efforts, Nif/T also works in tandem with patent attorneys and other IP technology specialists. Visit www.Nif-T.net.

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Industry	Computers , Electronics , Semiconductors , Technology , Wireless
Tags	Embedded Flash , non-volatile memory , flash memory , Flash , NVM , High-retention NVM , High-reliability NVM , High-temperature NVM , Low-voltage NVM
Link	http://prlog.org/12340020



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